

CLAIMS

- 1 1. A circuit comprising:
2 a semi-conducting substrate;
3 a coupler;
4 a detector, electrically connected to the coupler; and
5 the coupler and detector integrated into the semi-conducting substrate.
- 1 2. A circuit, as defined in claim 1, further comprising a power amplifier,
2 interposing the coupler and the detector, integrated into the semi-conducting substrate.
- 1 3. A circuit, as defined in claim 1, further comprising a charge pump, electrically
2 connected to the coupler and detector at a node, operative to raise the voltage at the node,
3 integrated into the semi-conducting substrate.
- 1 4. A circuit, as defined in claim 1, wherein the semi-conducting substrate is
2 selected from a group that includes Silicon and Gallium Arsenide.